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APPL NO.	FILING OR 371 (c) DATE	ART UNIT	FIL FEE REC'D	ATTY. DOCKET NO	DRAWINGS	TOT CLMS	IND CLMS
10/606,226	06/26/2003	2816	786	024016-00063	23	22	2

CONFIRMATION NO. 3751

004372
 ARENT FOX KINTNER PLOTKIN & KAHN
 1050 CONNECTICUT AVENUE, N.W.
 SUITE 400
 WASHINGTON, DC 20036

FILING RECEIPT



OC000000010852579

Date Mailed: 09/10/2003

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Applicant(s)

Hideaki Watanabe, Kasugai, JAPAN;

Assignment For Published Patent Application

FUJITSU LIMITED;

Domestic Priority data as claimed by applicant

Foreign Applications

JAPAN 2002-192559 ~~01/07/2002~~ 07/01/2002

If Required, Foreign Filing License Granted: 09/09/2003

Projected Publication Date: 12/18/2003

Non-Publication Request: No

Early Publication Request: No

Title

Clock multiplication circuit

Preliminary Class

327

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Title 37, Code of Federal Regulations, 5.11 & 5.15**

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the application of:

Hideaki WATANABE

**ATTN: Office of Initial Patent
Examination's Filing Receipt Corrections**

Serial Number: 10/606,226

Confirmation Number: 3751

Filed: June 26, 2003

Attorney Docket Number: 024016-00063

For: CLOCK MULTIPLICATION CIRCUIT

REQUEST FOR CORRECTED FILING RECEIPT

Commissioner for Patents
P.O. Box 1450
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Date: September 17, 2003

Sir:

A corrected filing receipt is hereby requested in view of the errors which appear in the Official Filing Receipt. In support of this request, enclosed are a copy of the Official Filing Receipt, on which the corrections have been highlighted in yellow, and a copy of the originally filed Declaration and Power of Attorney for Patent Application.

Please charge any deficiency or credit any overpayment with respect to this paper to Deposit Account Number 01-2300.

Respectfully submitted,


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